

PATENT NUMBER

O.I.P.E.	PATENT DATE
TR SCANNED <u>693</u> G.A. <u>M</u>	

APPLICATION NO. 09/753766	CONT/PRIOR	CLASS 326 712	SUBCLASS 228	ART UNIT 2819 2183	EXAMINER
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APPLICANTS

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Thomas Willis

TIME

Apparatus and method for concealing switch latency

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
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